IN THE SPECIFICATION

Please amend paragraph [0035] as follows:

Figure 7 illustrates a process for an embodiment of the invention. Process 700 begins with block ~~170~~710 which, starts the execution of a program thread by a first processor, such as commit processor 110. Block 720 performs fetching of commands by the first processor. Block 730 performs decoding of commands by the first processor. Block 740 instructs a second processor, such as speculative processor 120, to begin program execution of the same thread as the first processor, but at a location further in the program stream. Block 750 begins execution of the program thread by the second processor. On block 751 the second processor fetches commands. In block 752, the second processor performs decoding.

Please amend paragraph [0036] as follows:

In block 753, the second processor updates a register file. In block 754, the second processor transmits control flow information to the first processor. In block 760, the first processor updates a register file. Block 770 determines whether the first processor has reached the same point of execution as the second processor. If block 770 determines that the first processor has not yet reached the same point in the program, process 700 continues with block 780 to continue execution. If block 770 determines that the first processor has reached the same point in the execution as the second processor, block 790 determines if the program is complete. If block 790 determines that the program is complete, process 700 stops, otherwise, process 700 continues at ~~A~~block 710.